

S/N 10/032349

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Andre Zaccarin

Examiner: Unknown

Serial No.: 10/032349

Group Art Unit: 2621

Filed: December 21, 2001

Docket: 884.447US1

Title: METHODS AND APPARATUS FOR MOTION ESTIMATION USING LINE
AND SEARCH AREA SUBSAMPLING

Assignee: Intel Corporation

Customer No: 21186

INFORMATION DISCLOSURE STATEMENT

RECEIVED

JAN 29 2004

Technology Center 2600

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

INFORMATION DISCLOSURE STATEMENT

Serial No :10/032349

Filing Date: December 21, 2001

Title: METHODS AND APPARATUS FOR MOTION ESTIMATION USING LINE AND SEARCH AREA SUBSAMPLING

Assignee: Intel Corporation

Page 2

Dkt: 884.447US1 (INTEL)

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

ANDRE ZACCARIN

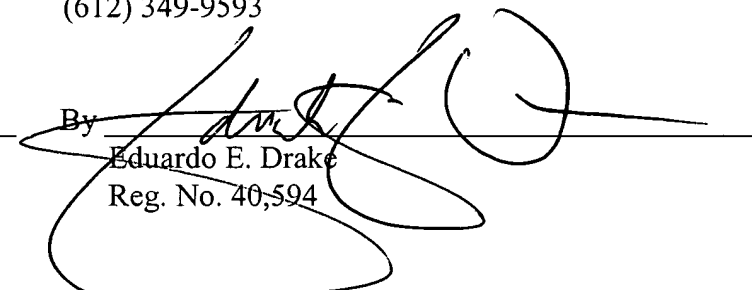
By his Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Attorneys for Intel Corporation
P.O. Box 2938
Minneapolis, Minnesota 55402
(612) 349-9593

Date

22 Jan 2004

By



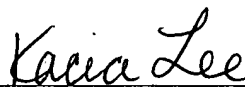
Eduardo E. Drake
Reg. No. 40,594

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 22 day of January, 2004.

Name

KACIA LEE

Signature



Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number 10/032349

Filing Date December 21, 2001

First Named Inventor Zaccarin, Andre

Group Art Unit 2621

Examiner Name Unknown

RECEIVED

JAN 29 2004

Sheet 1 of 1

Attorney Docket No: 884.447US1

Technology Center 2600

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-5,210,605	05/11/1993	Zaccarin, Andre, et al.	358	105	06/11/1991
	US-5,398,068	03/14/1995	Liu, Bede, et al.	348	416	09/02/1993

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
--------------------	---------------------	------------------	---	-------	----------	----------------

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		KIM, YANGHOON, et al., "A Block Matching Algorithm with 16:1 Subsampling and Its Hardware Design", <u>IEEE International Symposium on Circuits and Systems</u> , 1995, Vol. 1, (1995), 613-616	
		LIU, BEDE, et al., "New Fast Algorithms for the Estimation of Block Motion Vectors", <u>IEEE Transactions on Circuits and Systems for Video Technology</u> , Vol. 3, No. 2, (April 1993), 148-157	
		MOSCHETTI, FULVIO, et al., "A Fast Block Matching for SIMD Processors Using Subsampling", <u>Proceedings of the 2000 IEEE International Symposium on Circuits and Systems</u> , (May 28-31, 2000), pp. IV-321 to IV-324	

EXAMINER**DATE CONSIDERED**

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional) ² Applicant is to place a check mark here if English language Translation is attached